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PATENT C. Moore

SN 09/945,500

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes

Examiner: Ly D. Pham

Serial No.: 09/945,500

Group Art Unit: 2818

Filed: August 30, 2001

Docket No.: 1303.029US1

Title: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH
LOW TUNNEL BARRIER INTERPOLY INSULATORS

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AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
Washington, D.C. 20231

This paper is in response to the Office Action mailed on December 24, 2002. Please
amend the above-identified patent application as follows.

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